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PATENT / DOCKET NO. 16356.604 (DC-02762)
CUSTOMER NO. 000027683

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of:
La Vaughn F. Watts Jr., et al.

Serial No. 09/834,846

Filed: April 13, 2001

For: COMPUTER SYSTEM THERMAL
LAP MANAGEMENT METHOD AND
APPARATUS

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U.S. Patent No. 6,928,565

Issued: August 9, 2005

Certificate
SEP 28 2005
of Correction

**REQUEST FOR CERTIFICATE OF CORRECTION
UNDER 35 USC 254**

Mail Stop Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants hereby request a Certificate of Correction to correct mistakes in the above-identified patent as set forth on the attached form PTO/SB/44. A copy of Letter to Official Draftsperson, along with formal drawings 1 through 9, originally mailed on November 14, 2001, and a copy of return, date-stamped postcard, acknowledging receipt of these documents on January 8, 2002, are attached herewith in support of our Request for Certificate of Correction.

Because the mistakes were incurred through the fault of the Patent and Trademark Office, no fee is believed necessary. However, should any fees be deemed necessary, the Commissioner is hereby authorized to charge any fees which may be required to Deposit Account 08-1394 (16356.604).

Respectfully submitted,

James R. Bell
Registration No. 26,528

Date: 9-20-05

HAYNES AND BOONE, LLP
901 Main Street, Suite 3100
Dallas, Texas 75202-3789
Telephone: 512-867-8407
Facsimile: 214-200-0853

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
On	<u>9/20/05</u>
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Nishi Pasarya	

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,928,565
DATED : August 9, 2005
INVENTOR(S) : Watts, Jr., et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below.

Please replace drawing sheets 1 through 9, Figs. 1-9, as printed in the issued patent with drawing sheets 1 through 5, Figs. 1-9, that were submitted with the Letter to Official Draftsperson on November 14, 2001 (copy attached). Please also replace Fig. 1 as printed on the front page of the patent with the formal Fig. 1.

MAILING ADDRESS OF SENDER:

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PATENT NO. 6,928,565

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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Attorney docket no. 16356.604 (DC-02762)
Customer No. 000027683

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

La Vaughn F. Watts, Jr., et al.

Serial No.: 09/834,846

Filed: April 13, 2001

For: COMPUTER SYSTEM THERMAL LAP
MANAGEMENT METHOD AND APPARATUS

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Group Art Unit: 2181

Examiner: Unknown

LETTER TO OFFICIAL DRAFTSPERSON

Commissioner For Patents
Washington, DC 20231

Sir:

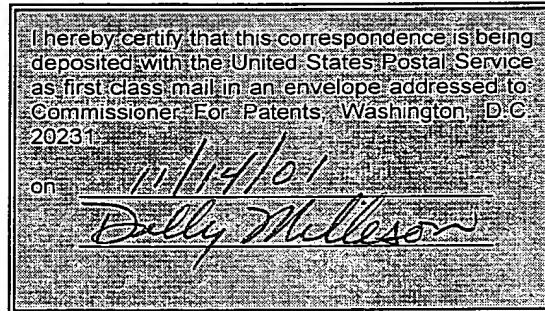
Enclosed are Five (5) sheets of formal drawings in connection with the above-identified patent application. Applicants respectfully request approval.

Respectfully submitted,

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A-122710.1



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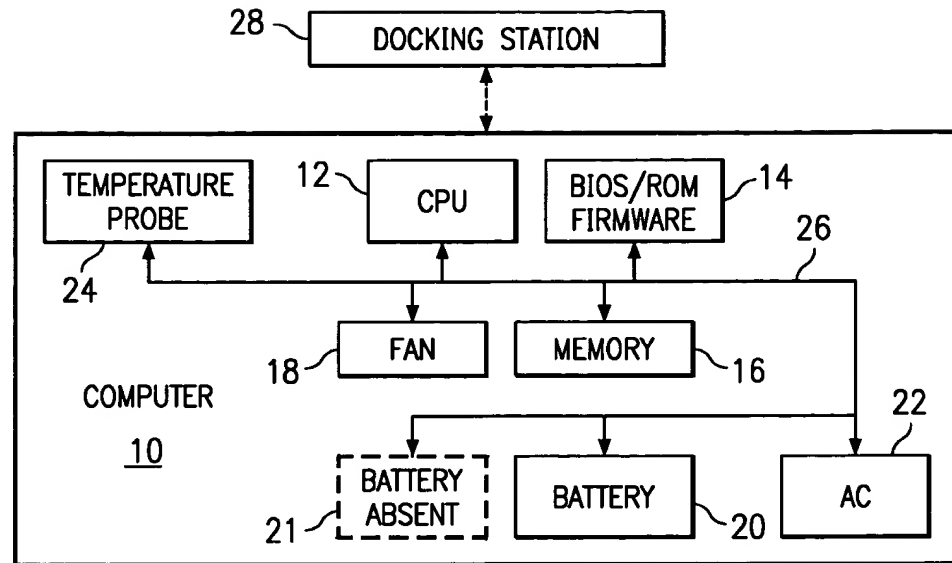


Fig. 2

COOL LAP 7	SMART CPU	HDD TIMER	MONITOR TIMER	SYSTEM TIMER	ACPI	AMBIENT	SURFACE TEMP
OFF	OFF	OFF	OFF	OFF	ON	22.8	23
TIME (MINUTES)	0	5	10	15	20	25	30
CPU TEMP	37	56	60	57	59	60	59
CPU TEMP IN F	98.6	132.8	140	134.6	138.2	140	138.2
CASE TEMP	24.8	28.2	31.8	34.6	36.3	38.1	38.9
CASE TEMP IN F	76.64	82.76	89.24	94.28	97.34	100.58	102.02
CPU SPEED	752	752	752	752	752	752	752
APPLICATION	EXCITE EXTREME 3D FASHION SHOW						

TIME (MINUTES)	35	40	45	50	55	60
CPU TEMP	53	59	51	60	59	58
CPU TEMP IN F	127.4	138.2	123.8	140	138.2	136.4
CASE TEMP	40.2	41.2	41.6	42.1	42.6	42.8
CASE TEMP IN F	104.36	106.16	106.88	107.78	108.68	109.04
CPU SPEED	752	752	752	752	752	752
APPLICATION	EXCITE EXTREME 3D FASHION SHOW					



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Fig. 3

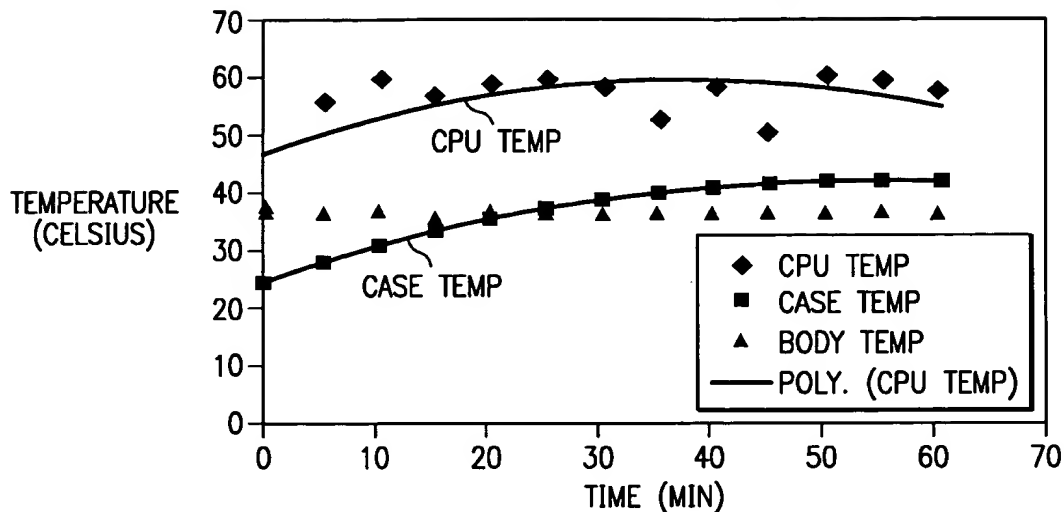
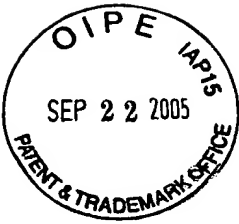


Fig. 4

COOL LAP 7b	SMART CPU	HDD TIMER	MONITOR TIMER	SYSTEM TIMER	ACPI	AMBIENT	SURFACE TEMP
ON	ON	OFF	OFF	OFF	ON	23.1	24.4
TIME (MINUTES)	0	5	10	15	20	25	30
CPU TEMP	32	48	47	48	49	49	50
CPU TEMP IN F	89.6	118.4	116.6	118.4	120.2	120.2	122
CASE TEMP	26.5	30	32.2	33.7	34.5	35.2	35.8
CASE TEMP IN F	79.7	86	89.96	92.66	94.1	95.36	96.44
CPU SPEED	693	694	604	604	605	602	604
APPLICATION	EXCITE EXTREME 3D FASHION SHOW						
TIME (MINUTES)	35	40	45	50	55	60	
CPU TEMP	49	49	50	50	50	50	
CPU TEMP IN F	120.2	120.2	122	122	122	122	
CASE TEMP	36.2	36.5	36.8	37	37.3	37.5	
CASE TEMP IN F	97.16	97.7	98.24	98.6	99.14	99.5	
CPU SPEED	604	603	608	604	604	604	
APPLICATION	EXCITE EXTREME 3D FASHION SHOW						

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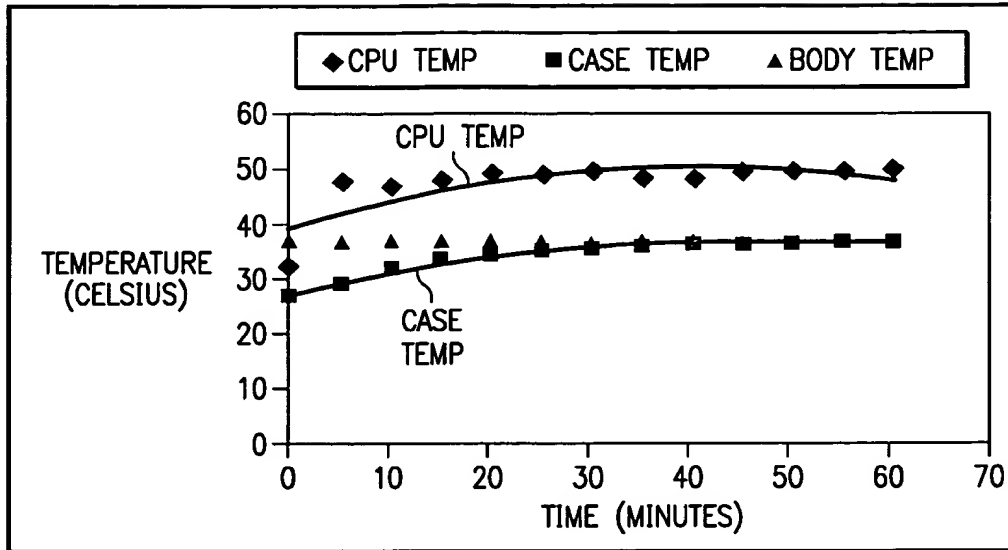
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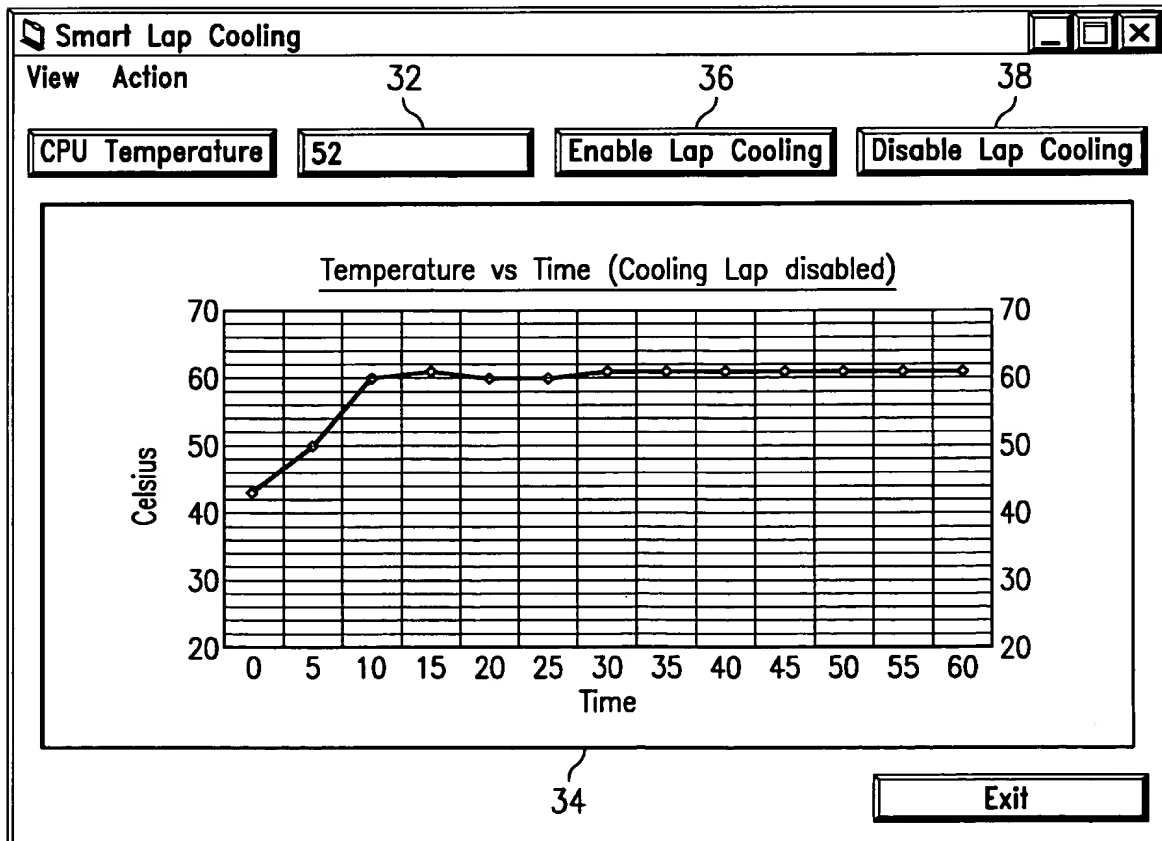
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Fig. 5



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Fig. 6



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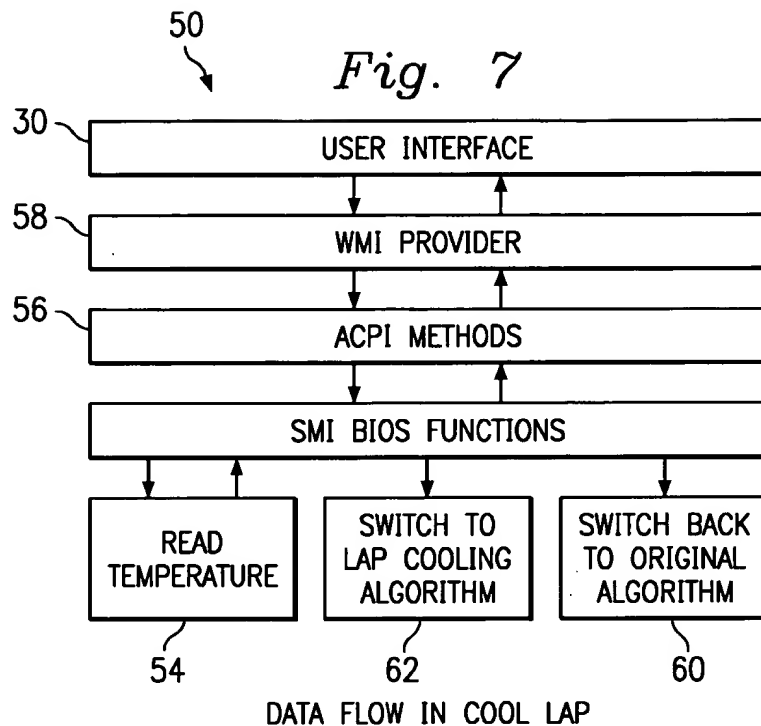


Fig. 8

LOWER RANGE	VALUE	HIGHER RANGE	VALUE	ACTION
TEMP_ABS_LOW:	-128	RANGE_1_HI:	23	No Action
RANGE_1_LO:	20	RANGE_2_HI:	25	No Action
RANGE_2_LO:	22	RANGE_3_HI:	26	TH1, Fan1 Low
RANGE_3_LO:	23	RANGE_4_HI:	27	TH1, Fan1 Hi
RANGE_4_LO:	24	RANGE_5_HI:	55	TH1, Fan1 Hi, Fan2 Low
RANGE_5_LO:	47	RANGE_6_HI:	65	TH2, Fan1 Hi, Fan2 Hi
RANGE_6_LO:	58	RANGE_7_HI:	90	TH3, Fan1 Hi, Fan2 Hi
RANGE_7_LO:	55	TEMP_CRITICAL-1:	101	TH4, Fan1 Hi, Fan2 Hi
RANGE_8_LO:	90	TEMP_CRITICAL:	102	TH4, Fan1 Hi, Fan2 Hi, ACPINOTIFY
				SwOff (Power off the unit)



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Fig. 9

LOWER RANGE	VALUE	HIGHER RANGE	VALUE	ACTION
TEMP_ABS_LOW:	-128	RANGE_1_HI:	23	No Action
RANGE_1_LO:	20	RANGE_2_HI:	25	No Action
RANGE_2_LO:	22	RANGE_3_HI:	26	TH1, Fan1 Low
RANGE_3_LO:	23	RANGE_4_HI:	27	TH1, Fan1 Hi
RANGE_4_LO:	24	RANGE_5_HI:	50	TH1, Fan1 Hi, Fan2 Low
RANGE_5_LO:	42	RANGE_6_HI:	60	TH2, Fan1 Hi, Fan2 Hi
RANGE_6_LO:	53	RANGE_7_HI:	75	TH3, Fan1 Hi, Fan2 Hi
RANGE_7_LO:	60	TEMP_CRITICAL-1:	101	TH4, Fan1 Hi, Fan2 Hi
RANGE_8_LO:	90	TEMP_CRITICAL:	102	TH4, Fan1 Hi, Fan2 Hi, ACPINOTIFY
				SwOff (Power off the unit)

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Fig. 1

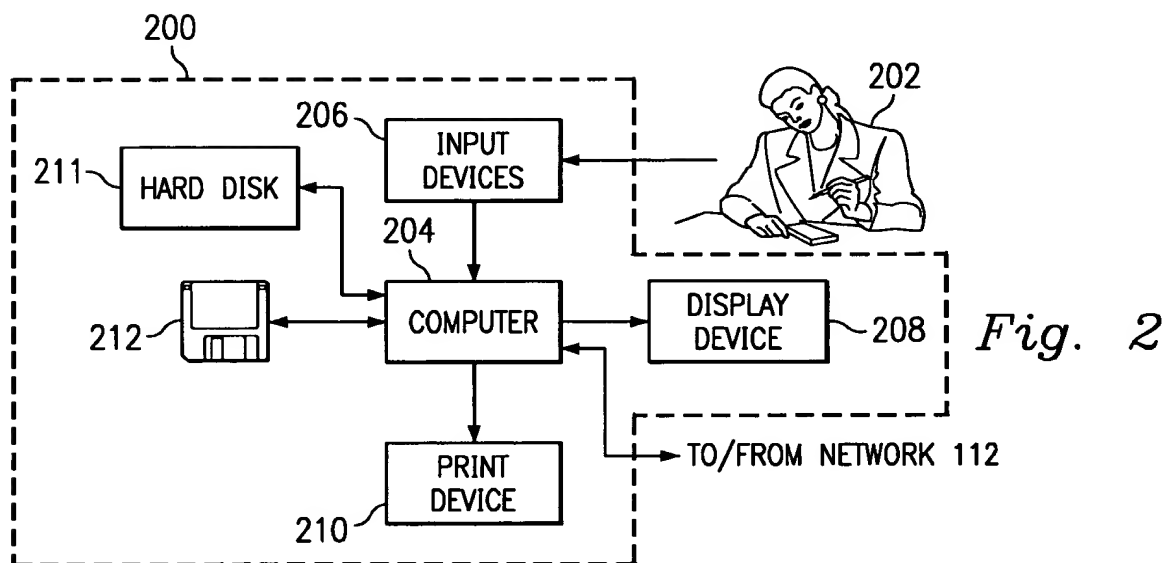
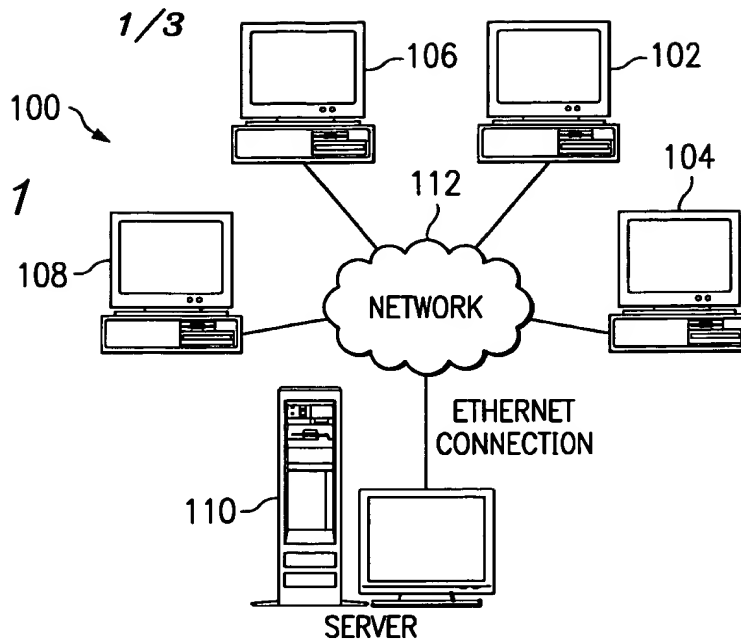
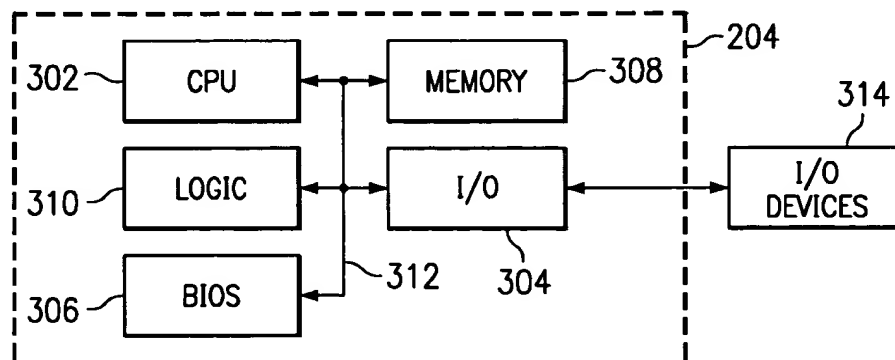


Fig. 3





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Fig. 4

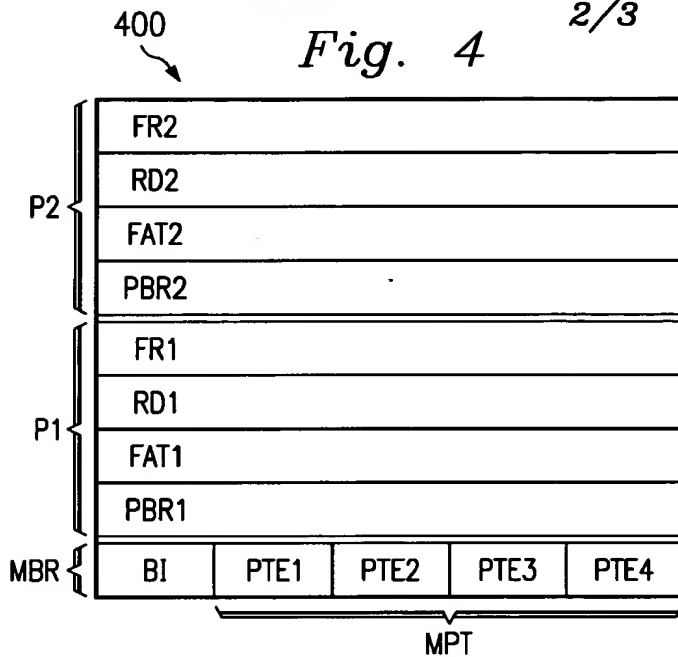
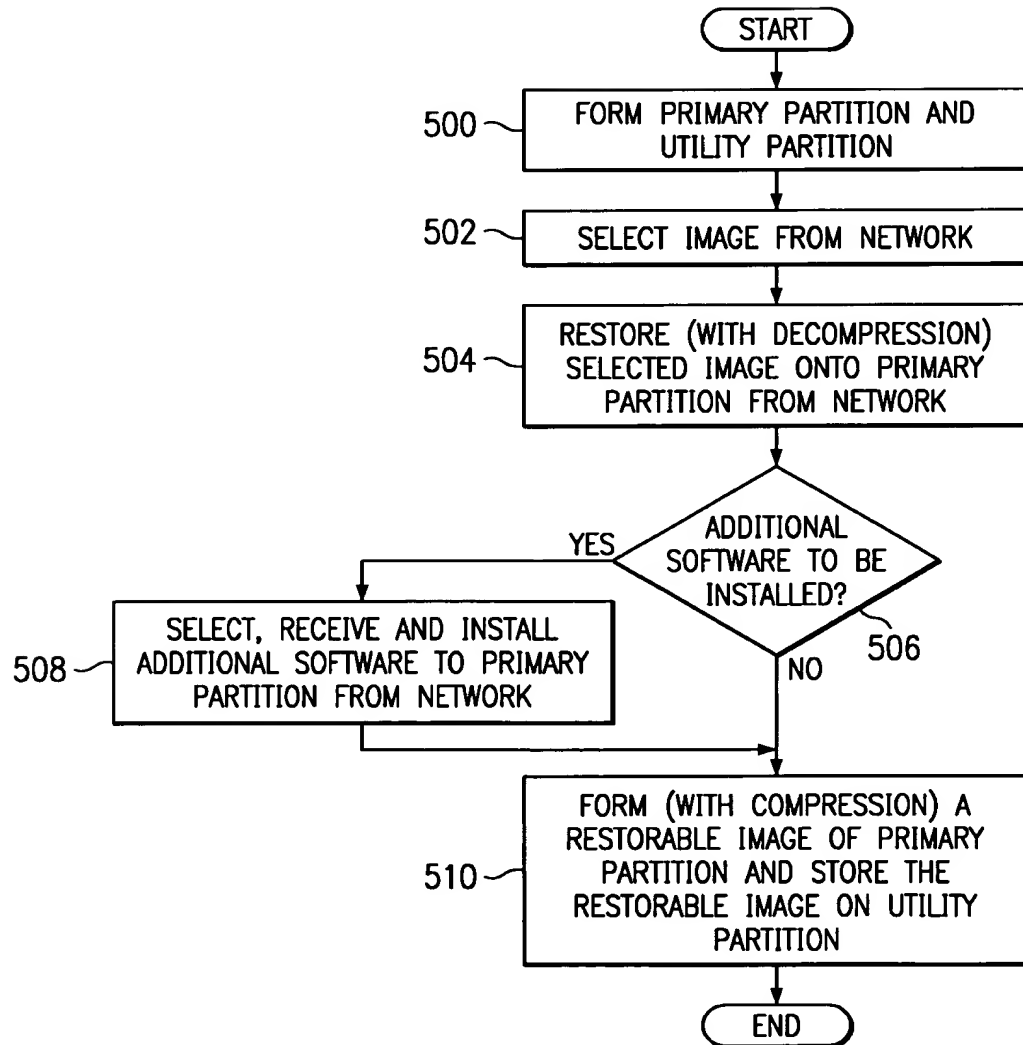


Fig. 5

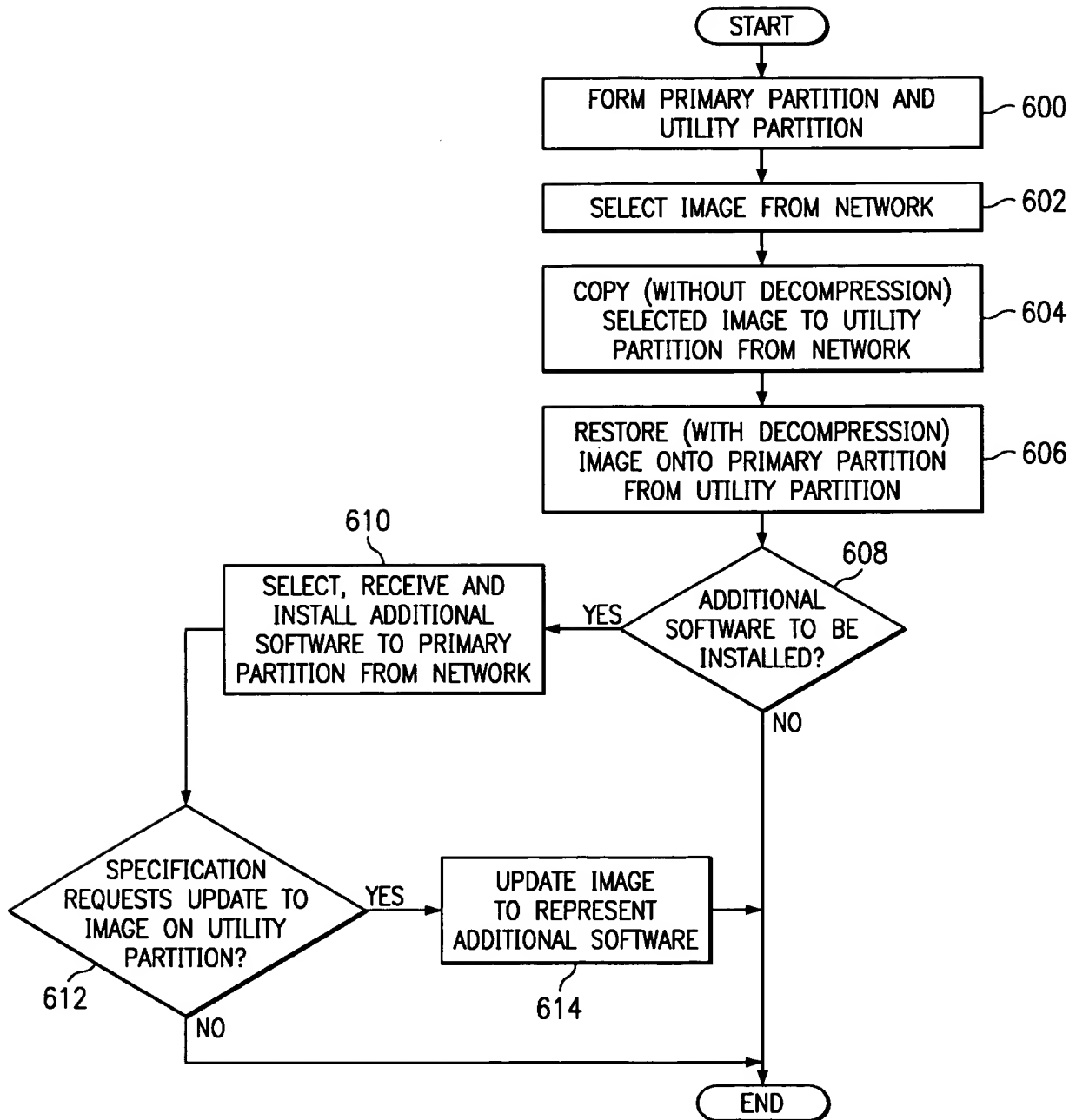




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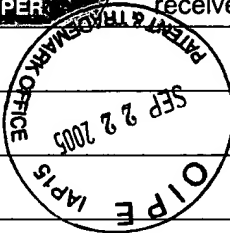
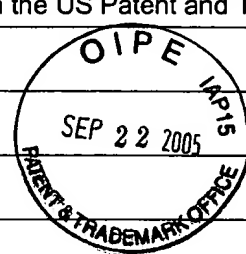
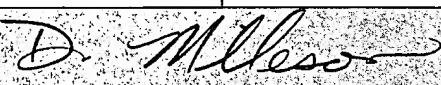
Fig. 6




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DESCRIPTION OF PAPER		A Letter to Official Draftsperson including five(5) drawing sheets and this return postcard were received in the US Patent and Trademark Office on the date stamped hereon.	
			
APPLICANT		Watts, Jr., et al.	
SERIAL NO.	09/834,846	DATE FILED	April 13, 2001
ATTORNEY DOCKET NO.	16356.604		
TITLE	COMPUTER SYSTEM THERMAL LAP MANAGEMENT METHOD AND APPARATUS		
SENDER'S INITIALS	JRB/kj/djm	DATE MAILED	11/14/01
OLD DATE(S)		NEW DATE(S)	POS
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		HAYNES & BOONE L.L.P.	
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SERIAL NO.	09/834,846	DATE FILED	April 13, 2001
ATTORNEY DOCKET NO.	16356.604		
TITLE	COMPUTER SYSTEM THERMAL LAP MANAGEMENT METHOD AND APPARATUS		
SENDER'S INITIALS	JRB/kj/djm	DATE MAILED	11/14/01 SEP 28 2005